Data Sheet



VIAVI

Xgig 16-lane CEM Type-B Interposer

for PCI Express® 4.0

Provides a tap of data signals between a host system and adapter card for the VIAVI Analyzer

VIAVI Solutions Xgig® Type-B Active Interposer, the Xgig4K-PCle4-X16-AIB, provides connection between the VIAVI PCle 4.0 Analyzer/Jammer chassis and a system under test. The Interposer installs into a 16-lane PCle CEM slot that is common on many servers, workstations, and desktop PCs. The Interposer provides a data signal tap for the Analyzer of communication between host and endpoint devices. It operates at the 16 GTps, PCle 4.0 rate, across 16-lanes (bi-directional).

This Interposer uses high-speed linear signal redrivers to buffer the system data signals across the Interposer between PCIe slot and PCB fingers. This design ensures a clean signal is delivered to the Analyzer and DUT devices for reliable protocol capture and analysis.



Key Features

- Supports link widths up to 16-lanes
- Operates up to 16 GTps, PCle 4.0 data rates
- Downward compatible with PCle gen-1, gen-2 and gen-3 data rates of 2.5, 5.0 and 8.0 GTps
- A top edge 16-lane CEM slot connector accepts all end-point DUT adapter cards
- Bottom edge 16-lane PCB finger connector installs into host test system's 16-lane PCIe slot
- Supplied with eight high-quality SFF-8674 equivalent custom cables for Analyzer attachment
- Data path uses high-speed linear redrivers to ensure good signal capture with minimal tuning effort
- Supports Analyzer side-band signal triggering and capture via custom cable
- Interposer power is independent of host system.
 A 120/220 AC in, 12V/3A DC out converter is included
- Custom, exchangeable brackets enable secure chassis and endpoint card installations
- LEDs give quick indication of power and operation
- Format: Design is based on the 16-lane Card Electro-Mechanical (CEM) specification
- Size: 210mm deep x 260mm tall x 15mm thick
- Works together with the VIAVI Xgig4K-PCle4-X16 Analyzer/Jammer platform chassis
- Provides consistent, repeatable capture of link training, equalization negotiation and other data
- Supports error injection for deep system analysis
- Supported by Xgig tool suite including Trace Control, Expert and Serialytics

Application

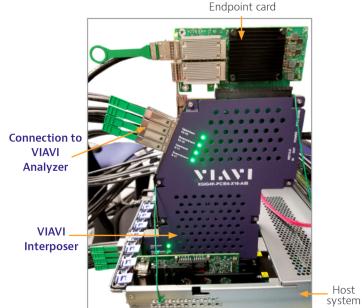
Working together with the VIAVI PCIe 4.0 Analyzer/ Jammer chassis, the Interposer enables debug and verification of new ICs, new system hardware implementations, FPGA firmware, validation of system BIOS and software, and supports manufacturing test.

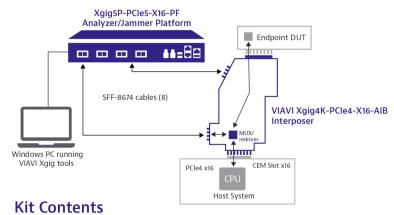
The Xgig4K-PCle4-X16-AIB Interposer is inserted into a 16-lane PCle slot in the host system, and an endpoint card is inserted into the top 16-lane slot connector creating the test data path. Cables connect the Interposer to the VIAVI protocol Analyzer/Jammer platform chassis.

The photo to the right shows a typical application, while the corresponding block diagram below indicates the signal data paths. Note that this Interposer requires an open chassis while in use.

When operating in Analyzer mode, the DUT data is captured (tapped) from both the upstream and downstream data paths as inputs to the Analyzer. When operating in Jammer mode, or Analyzer/Jammer mode, the Interposer data path is switched to route data into and out of the Analyzer.

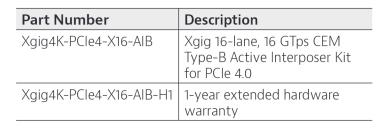
In addition to this 16-lane CEM Interposer, VIAVI offers a variety of other Interposer types for connecting into many popular PCle system applications, including U.2, M.2, OCulink, Flying-Lead and others.





Item	Description	Qty
1	Xgig4K-PCle4-X16-AIB, 16-lane CEM* Type-B Interposer card for PCle 4.0	1
2	SFF-8674 custom high-performance cables	8
3	Sideband cable	1
4	MCX coax cables (for optional clock)	2
5	Power brick, 120/220 input, 12V/3A output	1
6	Replaceable bracket kit	1

Ordering Information



^{*}CEM: Card Electro-Mechanical Specification (a PCle spec)







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